



BUCKET NO.: NATI15-05062
Customer No.: 23990

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : HAL C. MCCOWN
U.S. Serial No. : 10/032,208
Filed : October 26, 2001
For : SYSTEMS FOR SELECTIVELY DISABLING TIMING VIOLATIONS
IN HARDWARE DESCRIPTION LANGUAGE MODELS OF
INTEGRATED CIRCUITS AND METHODS OF OPERATING THE
SAME
Art Unit : 2123
Examiner : Thomas H. Stevens

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Sir:

The undersigned hereby certifies that the following documents:

1. Response to Non-Compliant Amendment;
2. Substitute Specification, substitute Claims, substitute Abstract, and two (2) sheets of substitute/replacement drawings; and
3. A postcard receipt

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 1/17/06.

Date: 1/17/06

Kathy Cedar
Mailer

Date: Jan. 17, 2006

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DOCKET NO. P05062

PATENT

SYSTEMS FOR SELECTIVELY DISABLING TIMING VIOLATIONS
IN HARDWARE DESCRIPTION LANGUAGE MODELS OF INTEGRATED CIRCUITS
AND METHODS OF OPERATING THE SAME

SUBSTITUTE DRAWINGS